

GR 98 P 8041



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By: Harold N. Niff

Date: Jan 29, 2000

GAU/2811 \$
Alt-2811
13/4
2-9-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Thomas Huttner et al.

Applic. No. : 09/313,424

Filed : May 17, 1999

Title : SOI Semiconductor Configuration And Method Of
Fabricating The Same

Examiner : Shouxiang Hu

Group Art Unit : 2811

A M E N D M E N T

Hon. Commissioner of Patents and Trademarks,
Washington, D. C. 20231

S i r :

Responsive to the non-final Office action dated September 26,
2000, kindly amend the above-identified application as
follows:

In the Claims:

~~Claim 7 (amended). A method of fabricating [the] a
semiconductor configuration [according to claim 1], which
comprises the following steps:~~